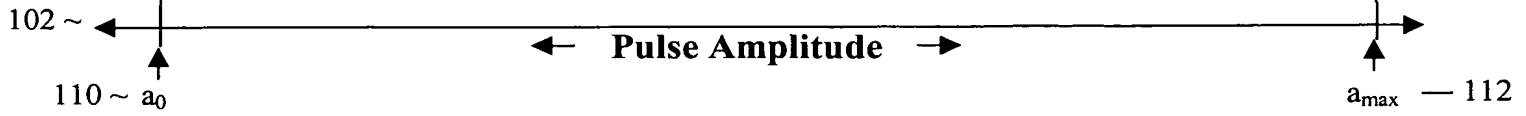
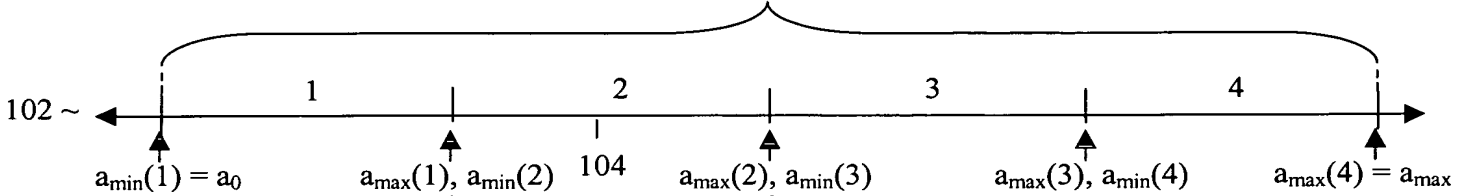


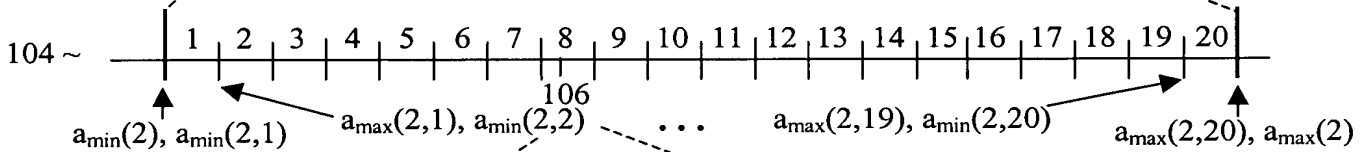
# Amplitude Range



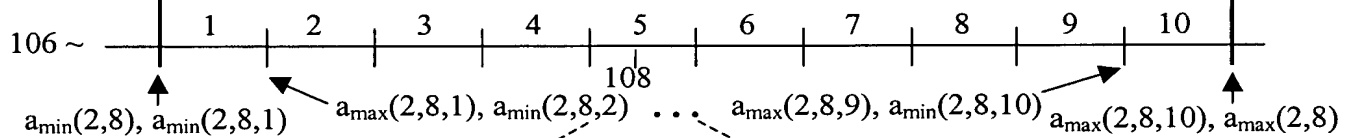
## Amplitude Range Subdivided Into Four Components, $N_{\text{components}} = 4$



## Amplitude Range Component 2 Subdivided Into Twenty Sub-components, $N_{\text{subcomponents}}(2) = 20$



## Sub-component 8 of component 2 Subdivided Into Ten Smaller Components, $N_{\text{smaller components}}(2,8) = 10$



108 ~ Smaller Component 5 of Sub-component 8 of Component 2  
Subdivided into X Even Smaller Components,  
 $N_{\text{even smaller components}}(2,8,5) = X$ , and so on.

FIGURE 1. Non-temporal Layout Parameters

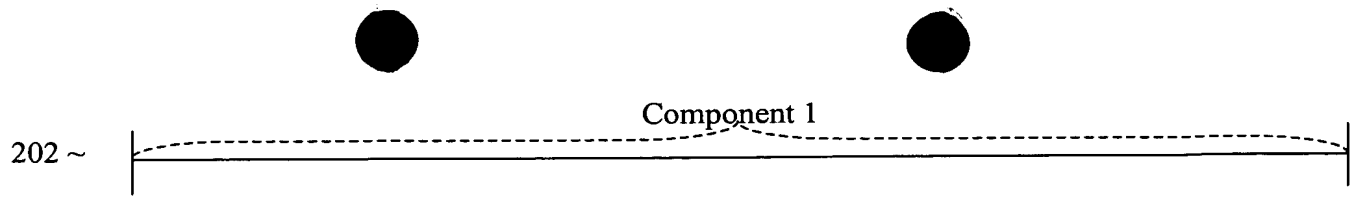


FIGURE 2a.

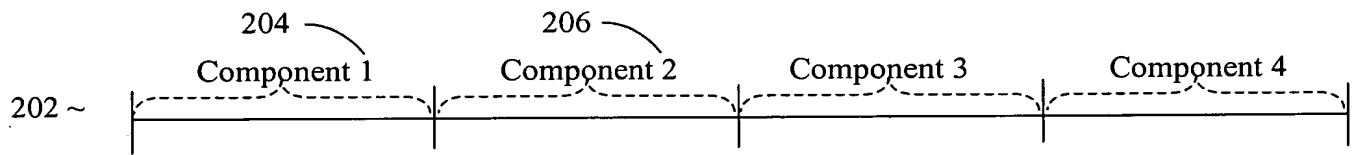


FIGURE 2b.

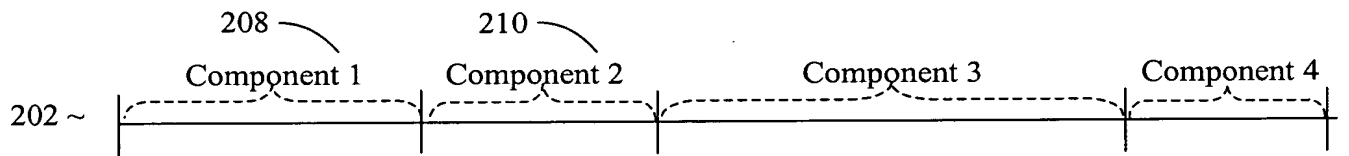
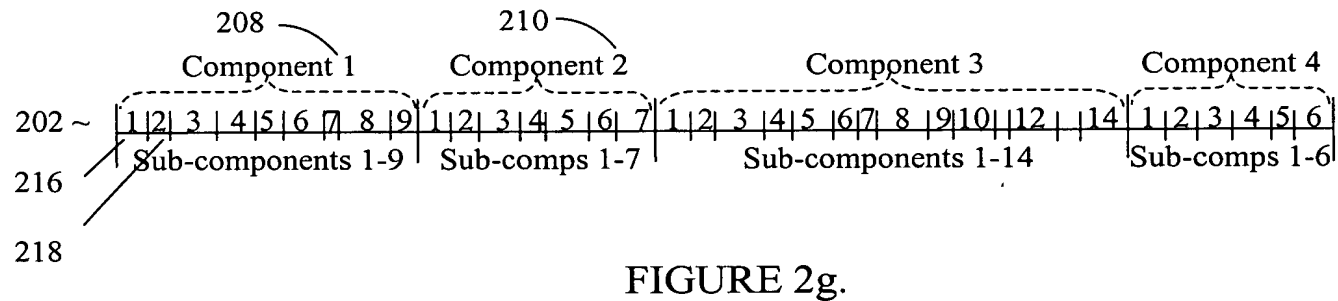
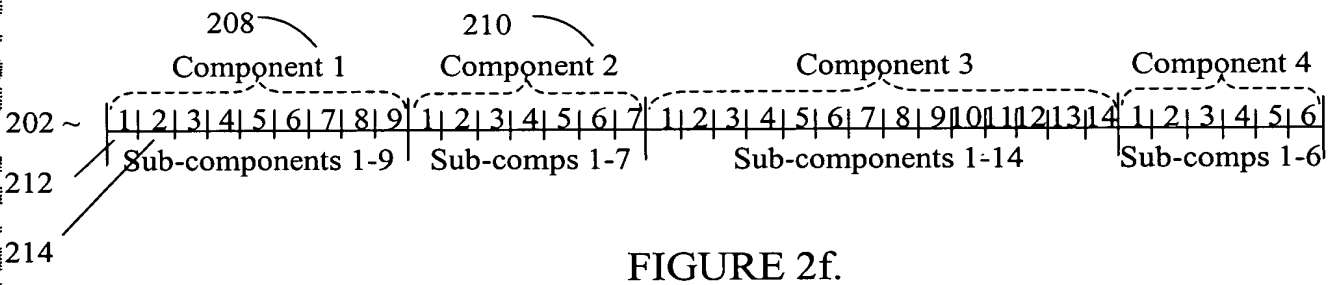
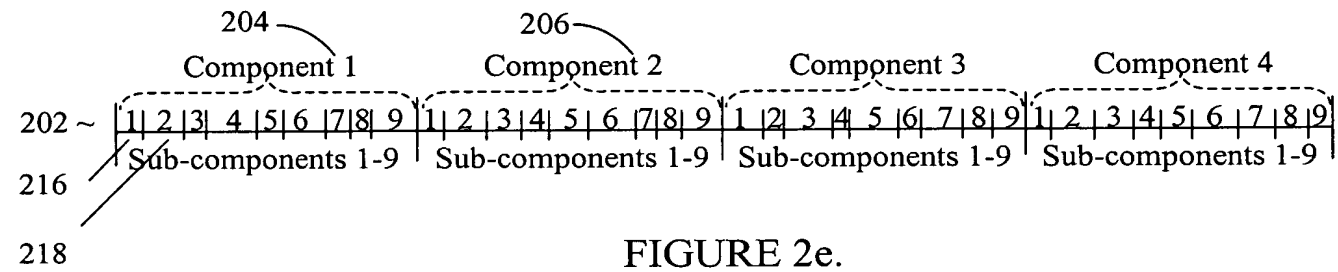
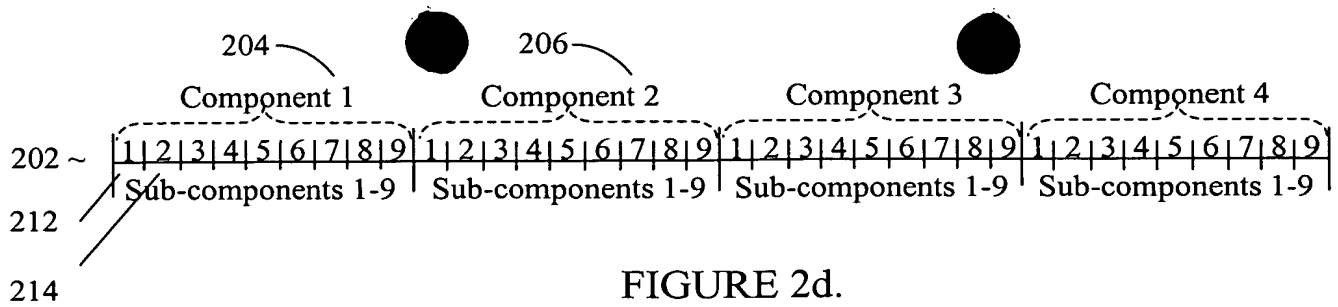


FIGURE 2c.

09638192 "081500  
005780 "267960



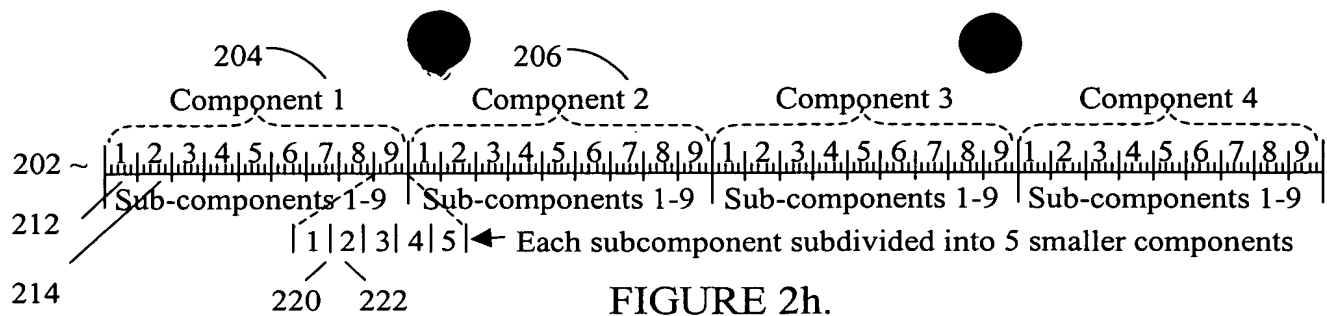


FIGURE 2h.

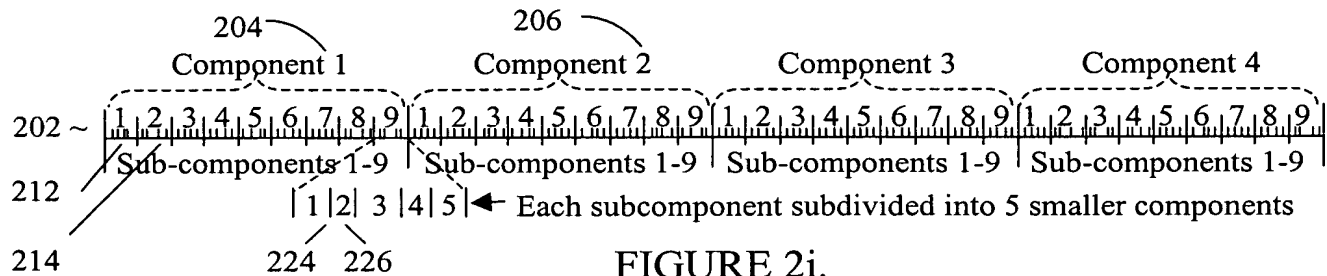


FIGURE 2i.

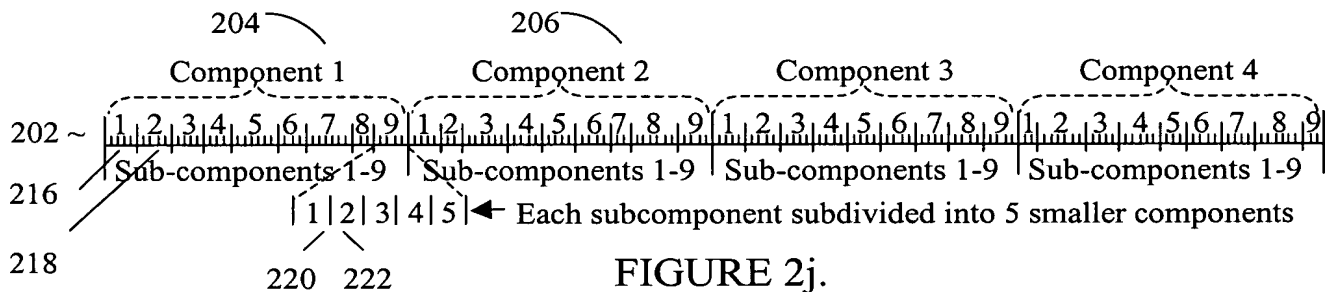


FIGURE 2j.

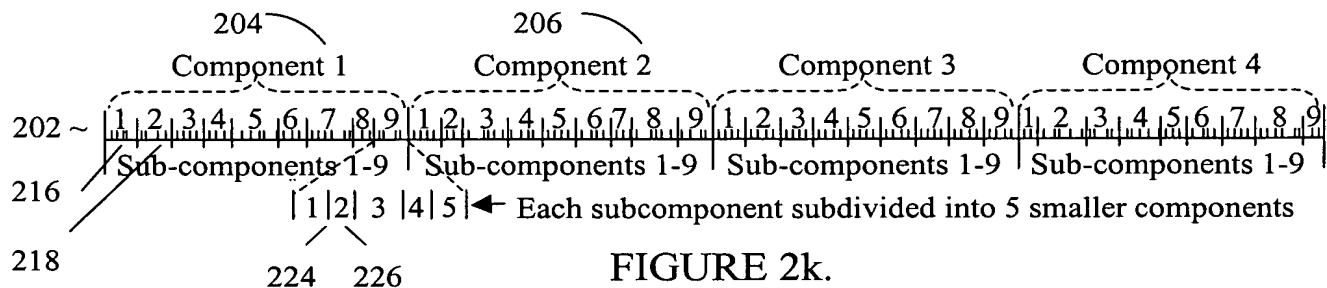


FIGURE 2k.

09638192 "081500

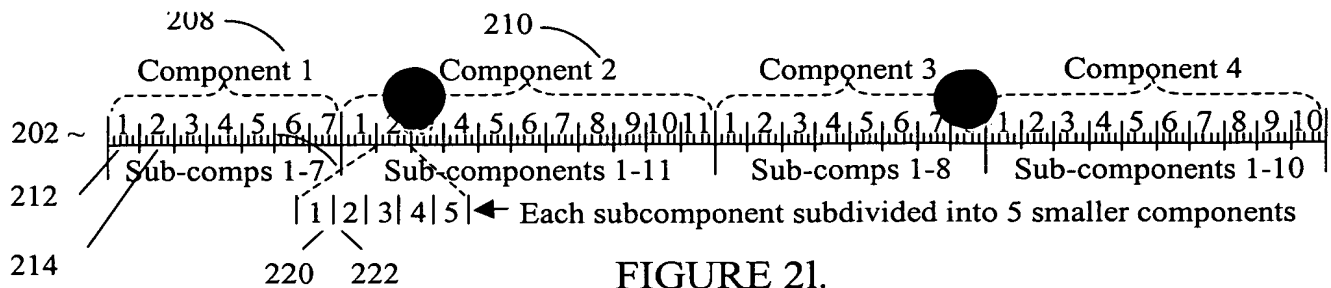


FIGURE 21.

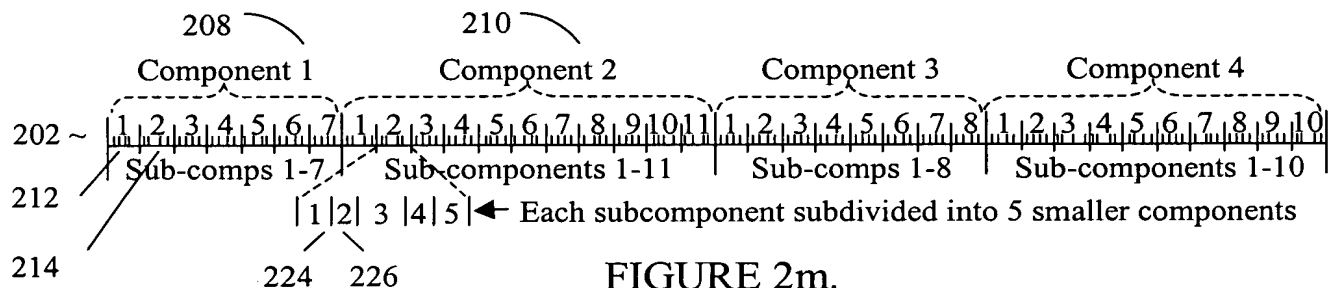


FIGURE 2m.

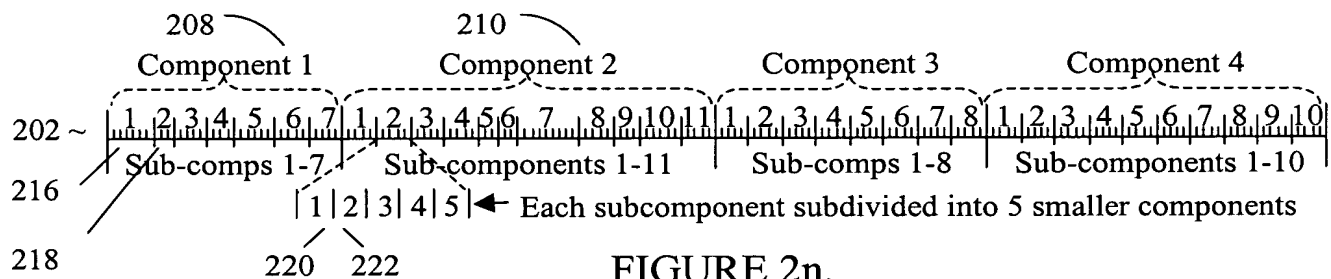


FIGURE 2n.

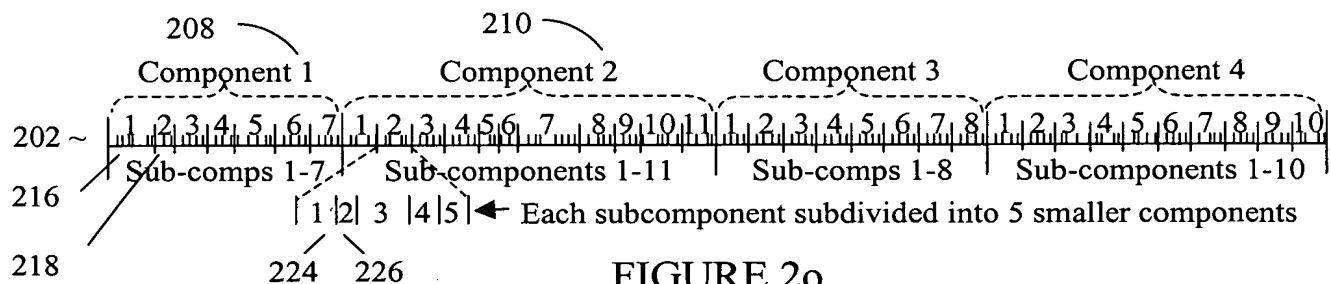


FIGURE 2o.

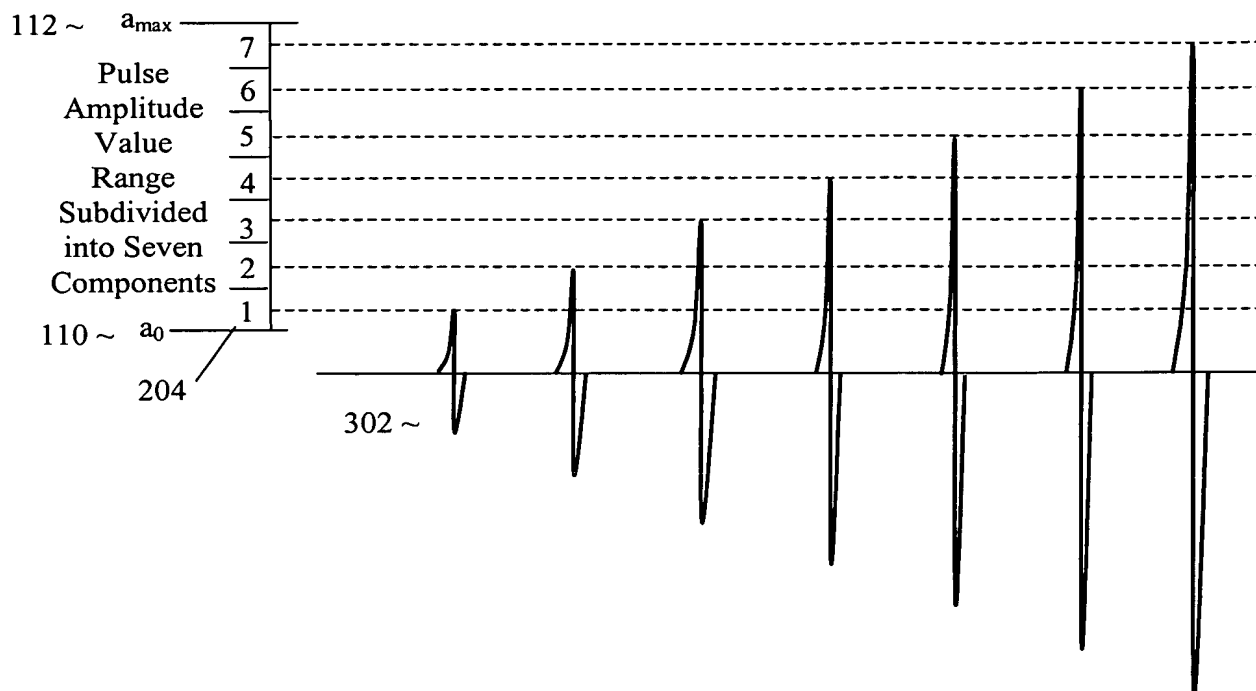


FIGURE 3a

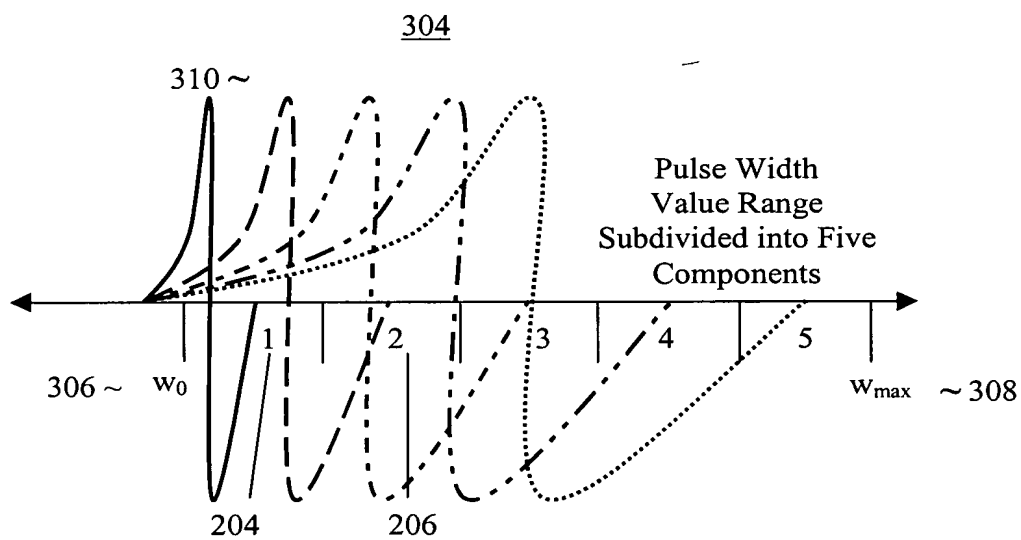
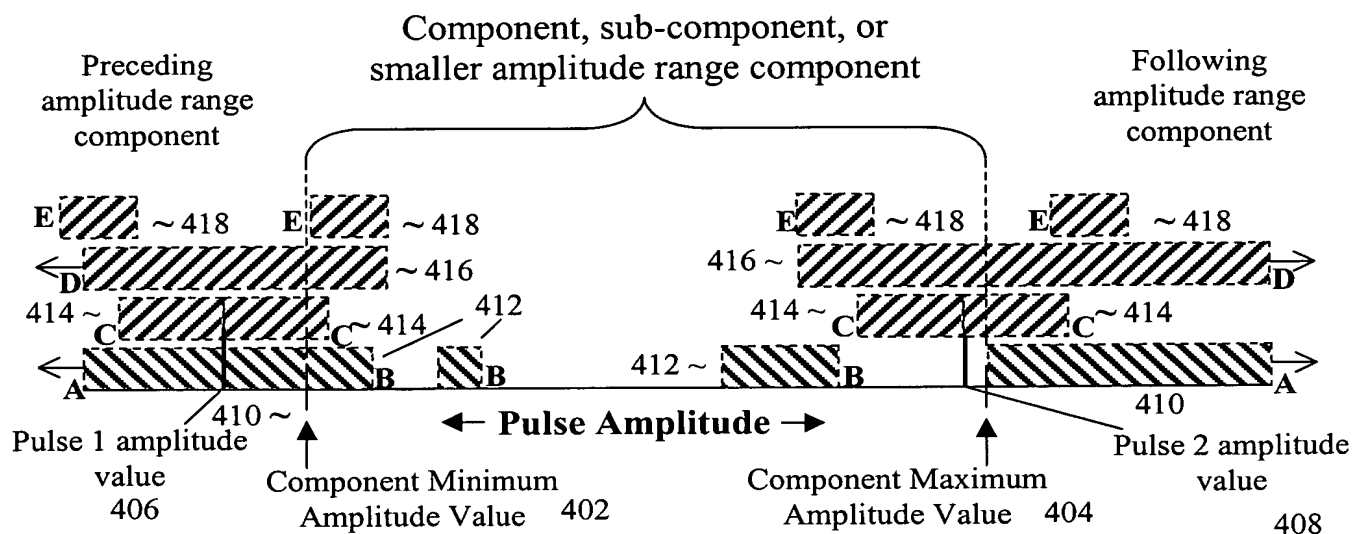


FIGURE 3b

00638192.081500

## Non-Allowable Regions - Viewed Separately



## Non-Allowable Regions - Combined

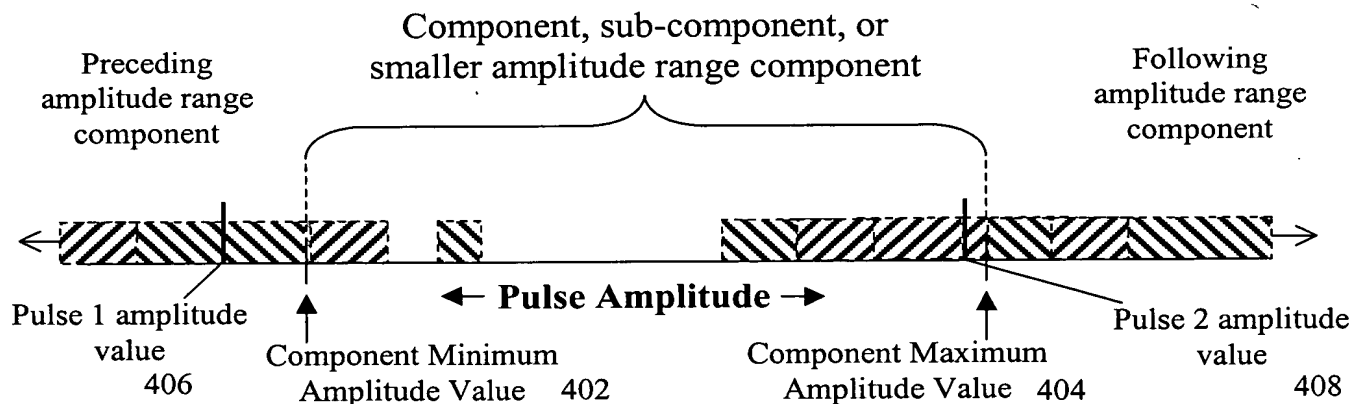
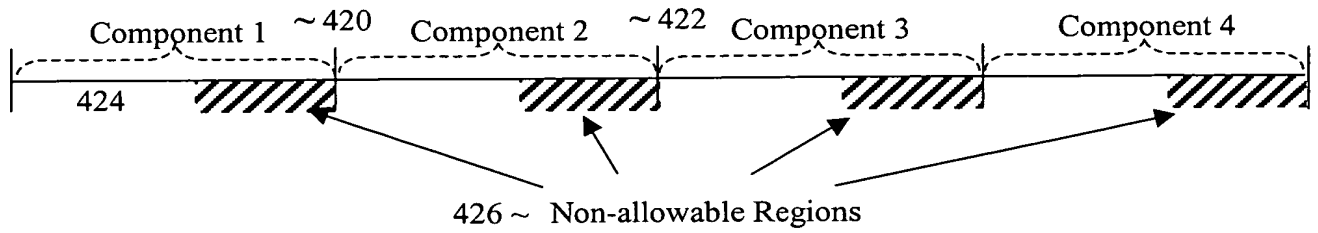


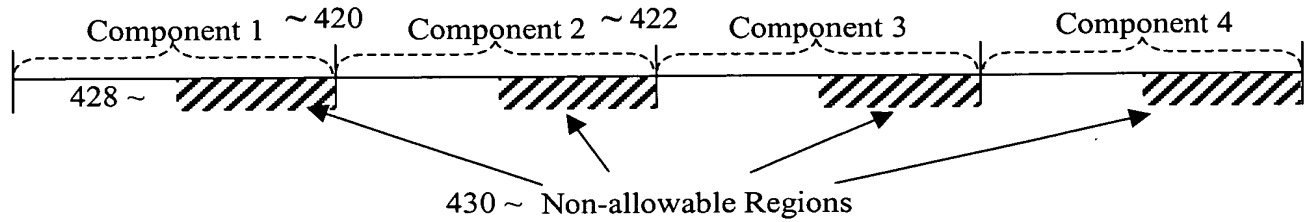
FIGURE 4a. Non-allowable Regions

09638192-081500

**Example 1: Non-allowable Regions of Smaller Size Than Allowable Regions**



**Example 2: Non-allowable Regions of Same Size as Allowable Regions**



**Example 3: Non-allowable Regions of Larger Size Than Allowable Regions**

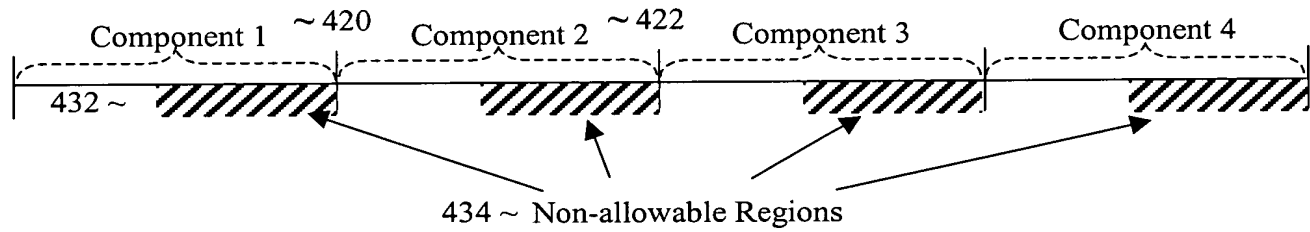


FIGURE 4b.

005180" 26T8E960



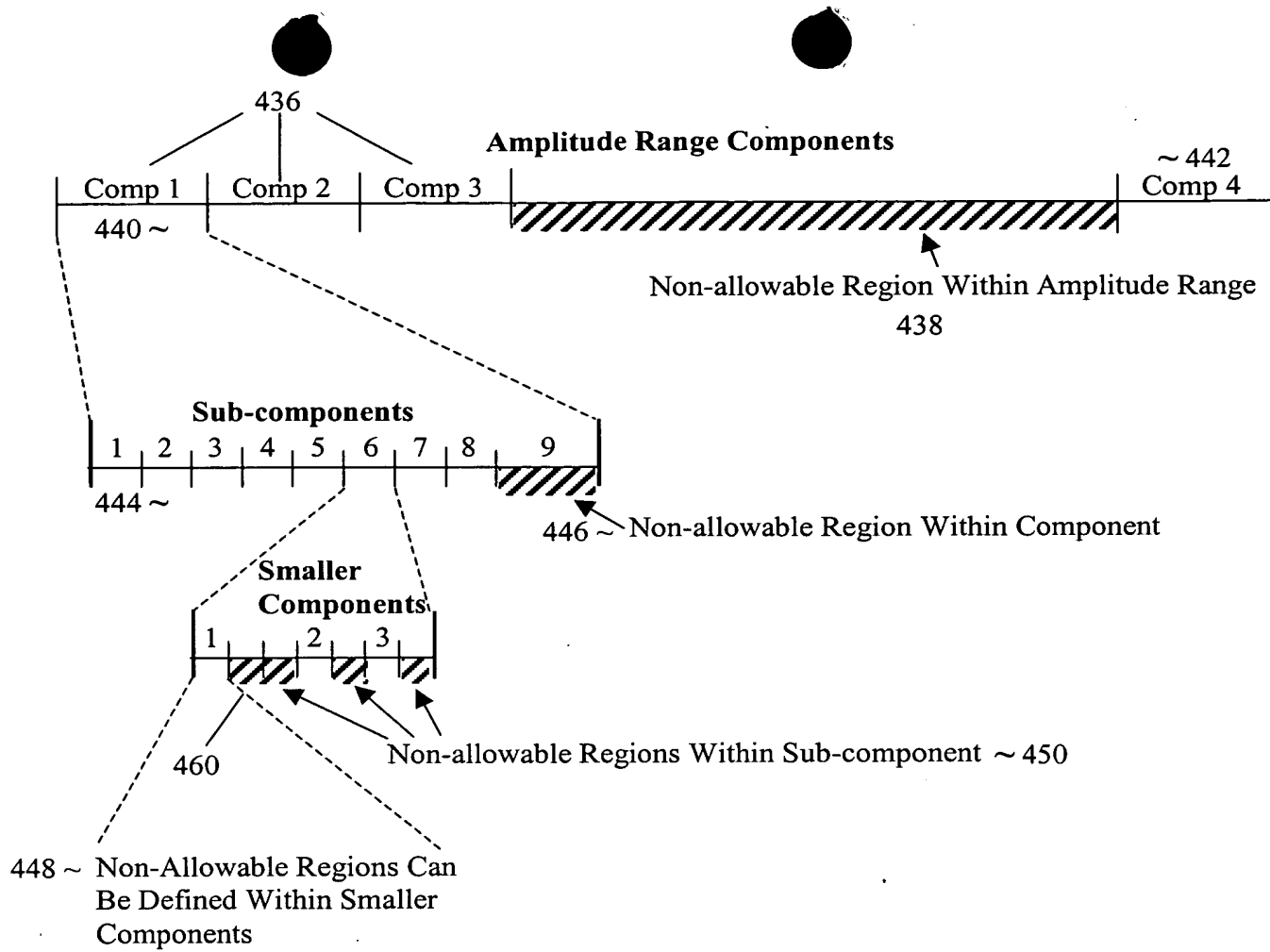


FIGURE 4c.

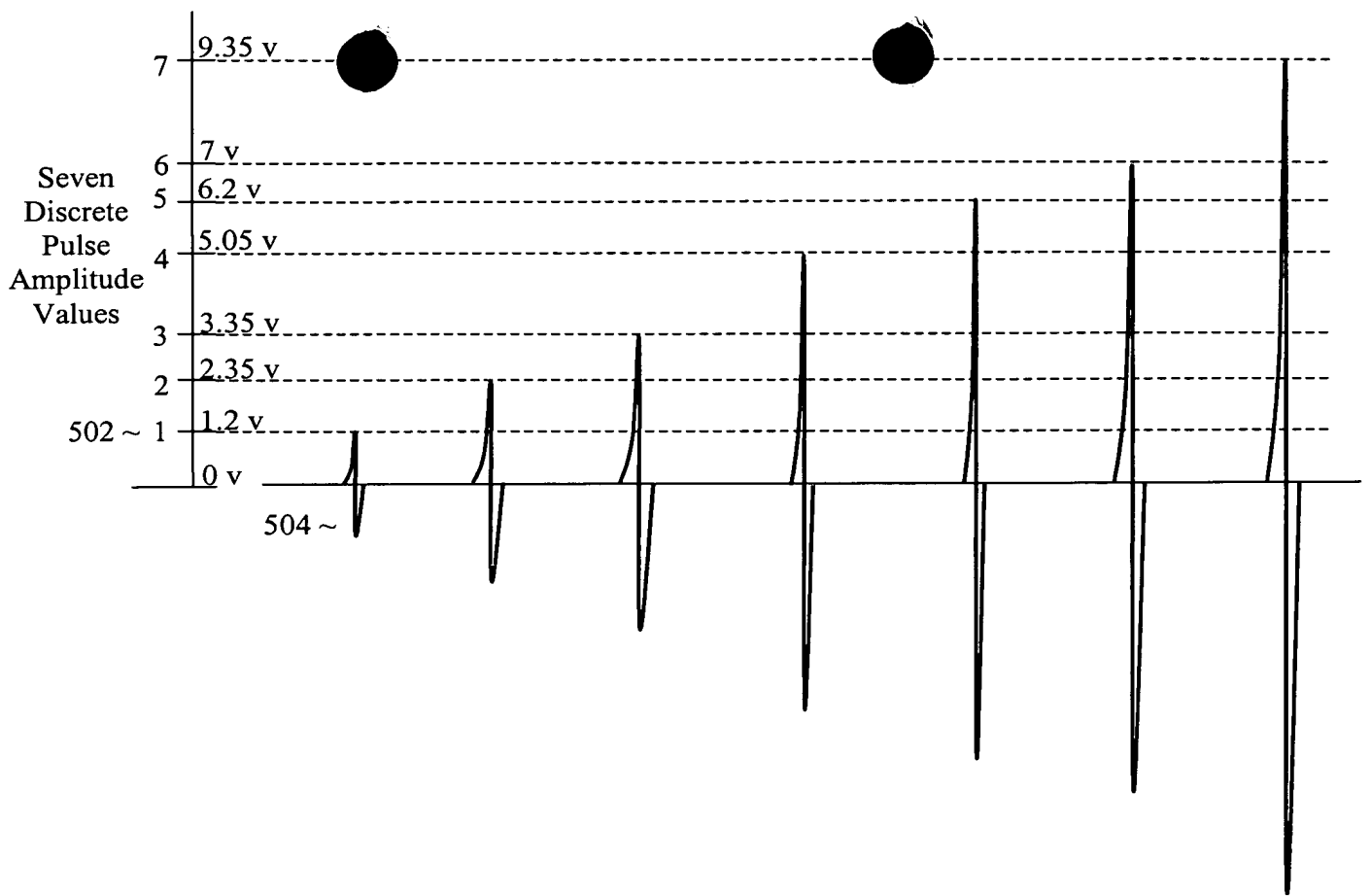


FIGURE 5a

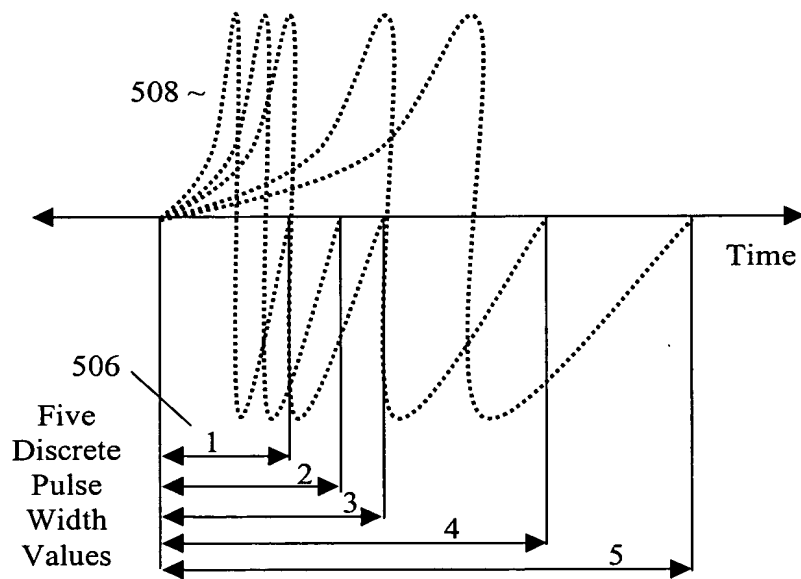


FIGURE 5b

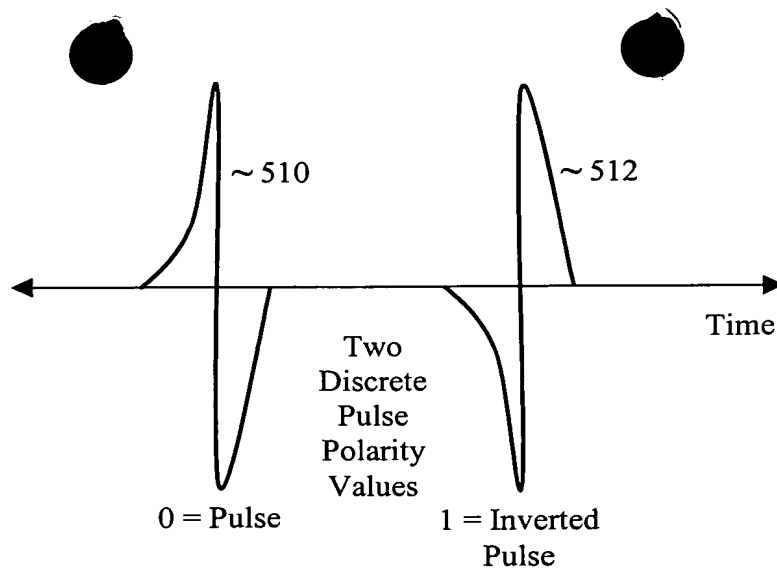


FIGURE 5c

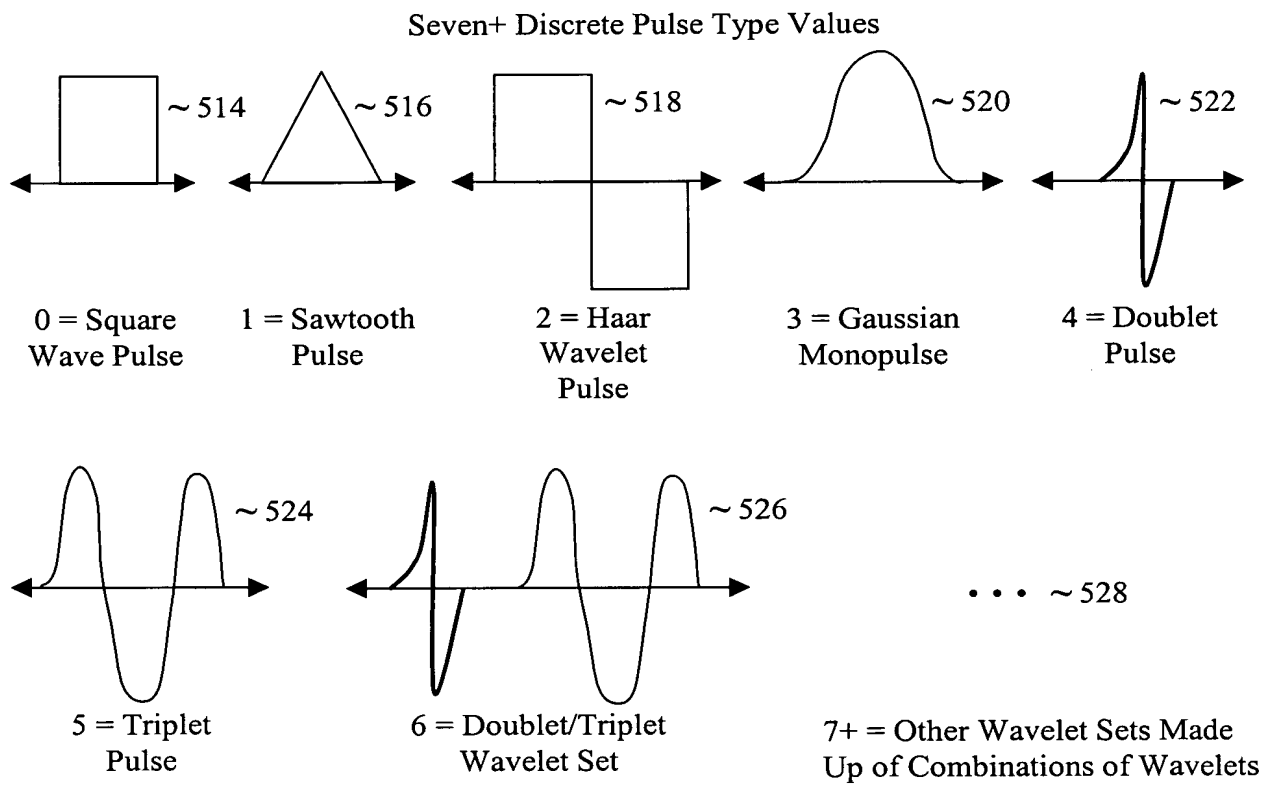


FIGURE 5d

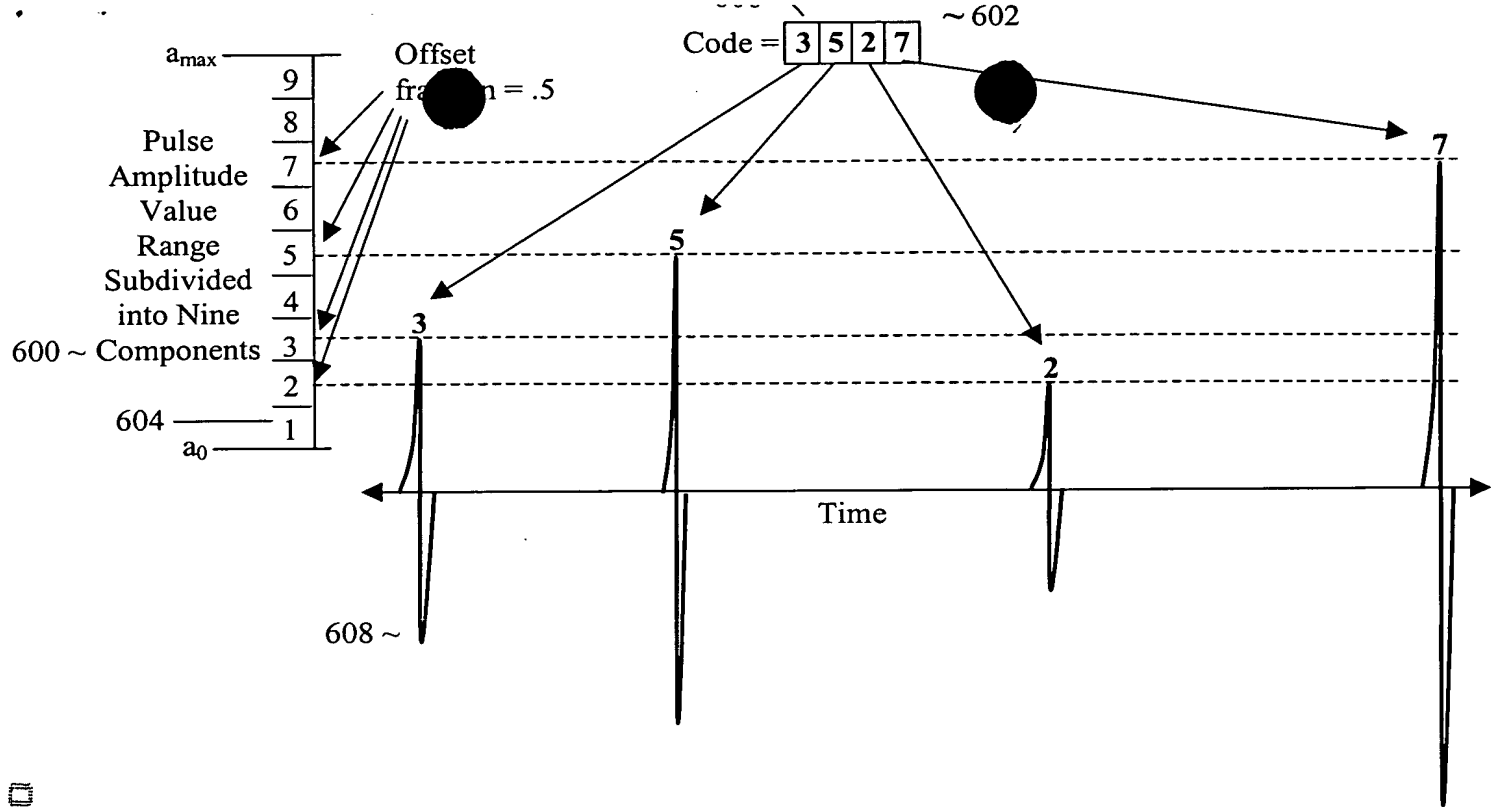


FIGURE 6a

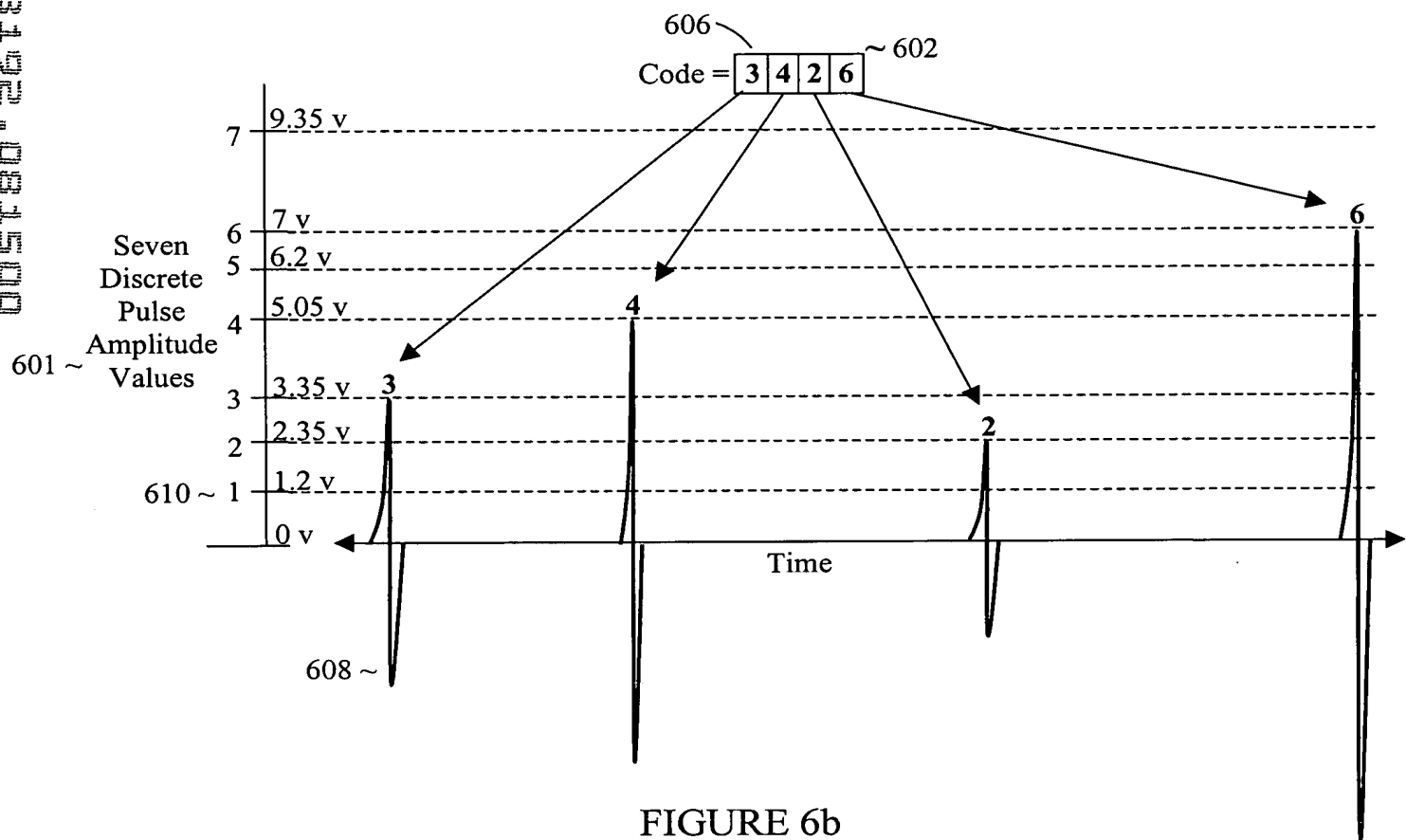


FIGURE 6b

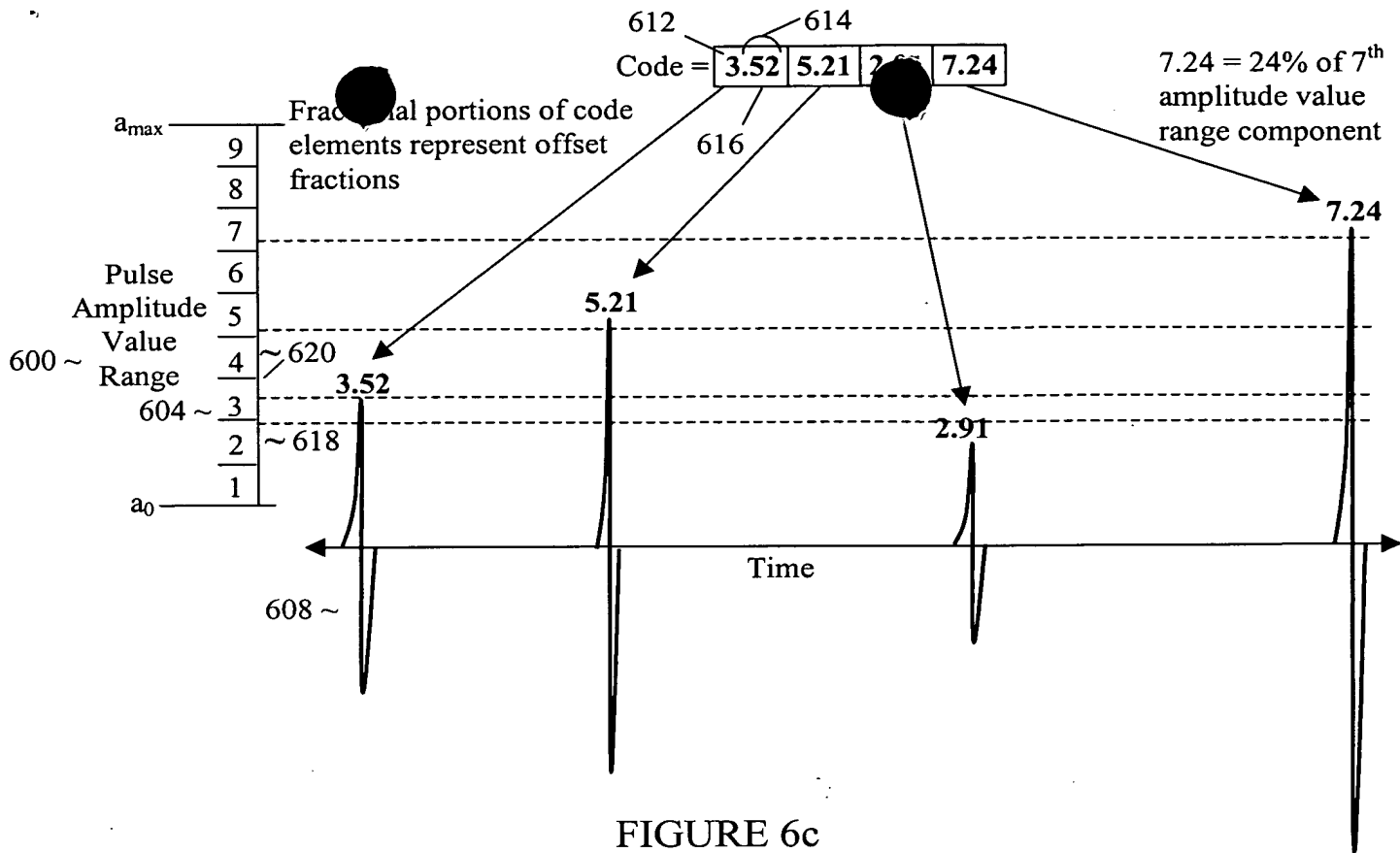


FIGURE 6c

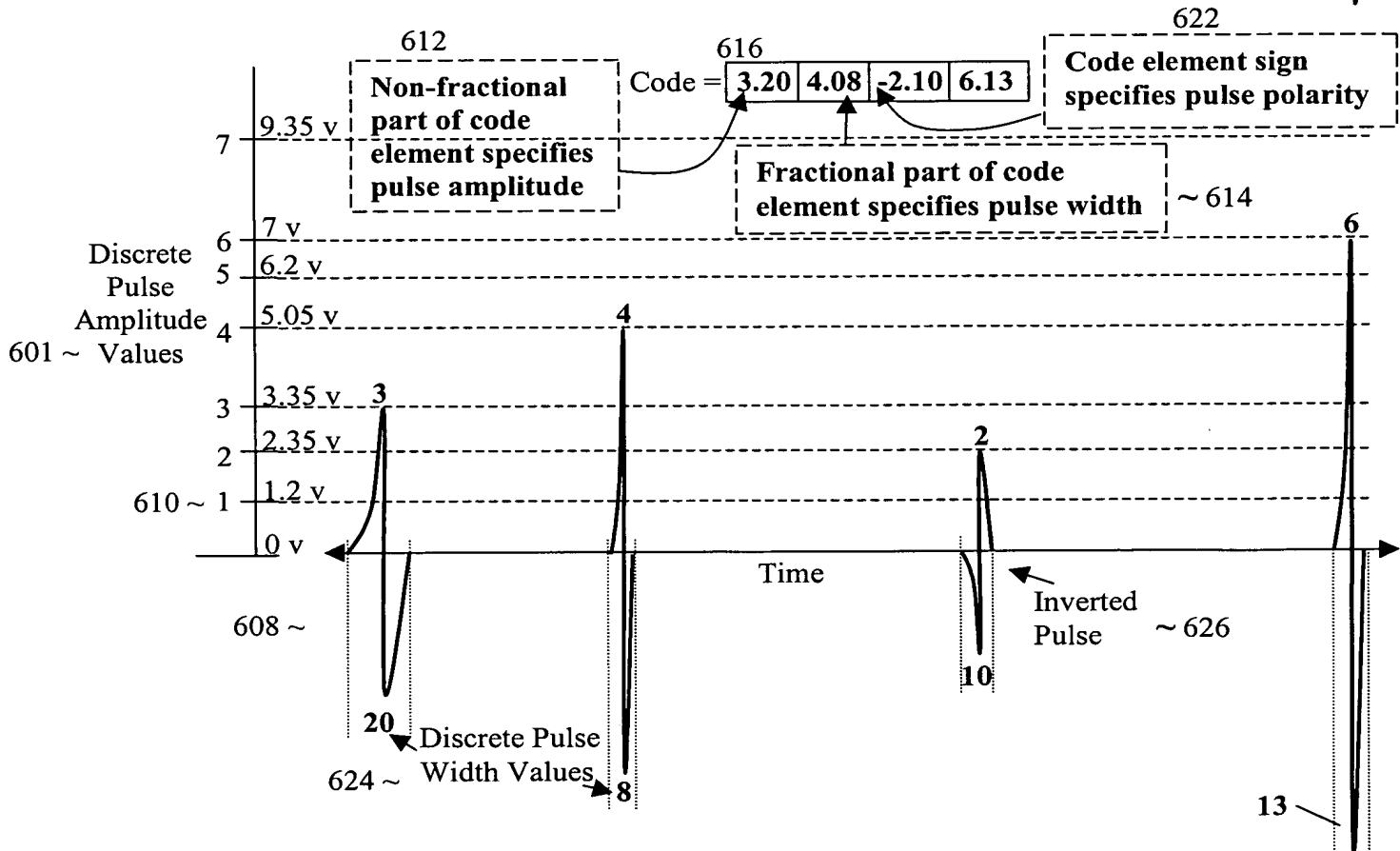


FIGURE 6d

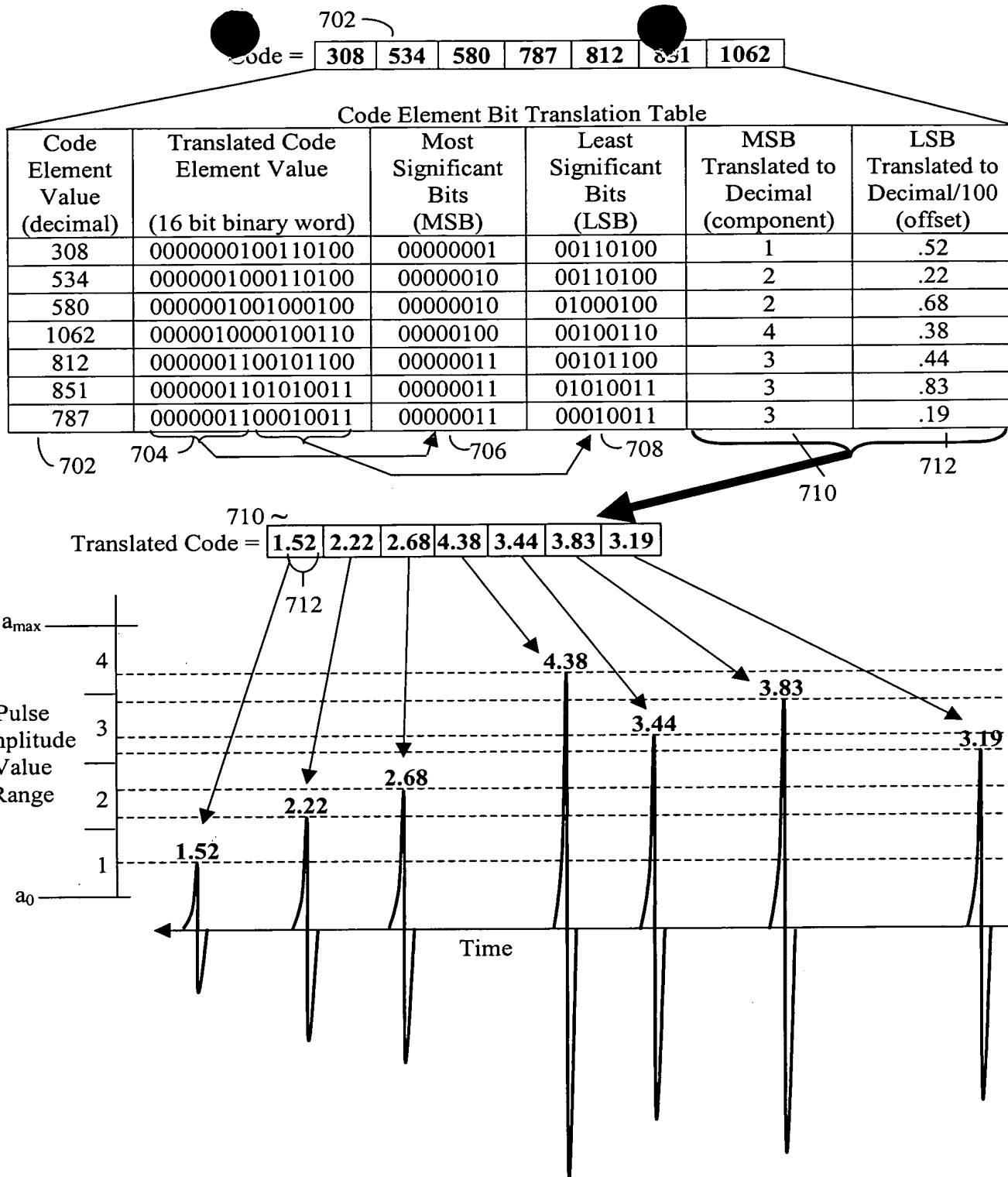


FIGURE 7a

Code = 361 616 648 806 857 934 1100

Code Element Bit Translation Table

Code Element Value (decimal)	Translated Code Element Value (16 bit binary word)	Most Significant Bits (MSB)	First Seven Least Significant Bits (LSB)	Last LSB (+/-)	MSB Translated to Decimal (component)	First 7 LSB Translated to Decimal/100 (offset)
361	0000000101101001	00000001	0110100	1	1	.52
616	0000001001101000	00000010	0110100	0	2	.22
648	0000001010001000	00000010	1000100	0	2	.68
1100	0000010001001100	00000100	0100110	0	4	.38
857	0000001101011001	00000011	0101100	1	3	.44
934	0000001110100110	00000011	1010011	0	3	.83
806	0000001100100110	00000011	0010011	0	3	.19

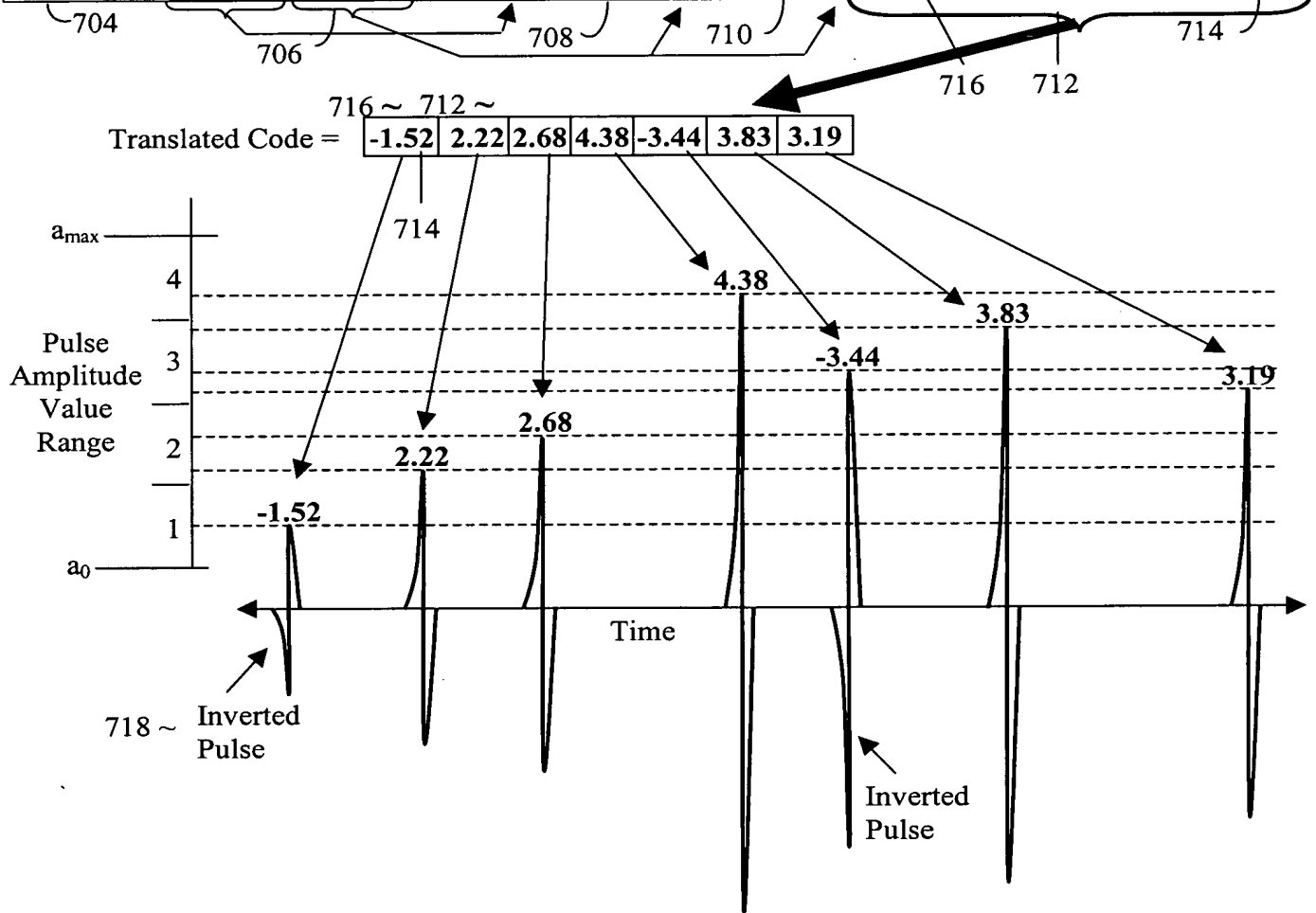


FIGURE 7b

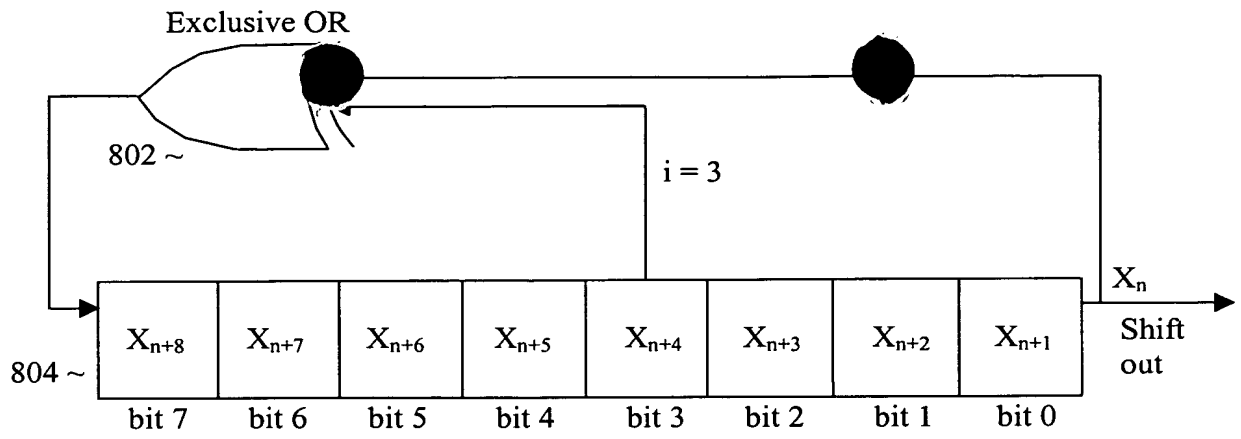


FIGURE 8a. Linear Feedback Shift Register

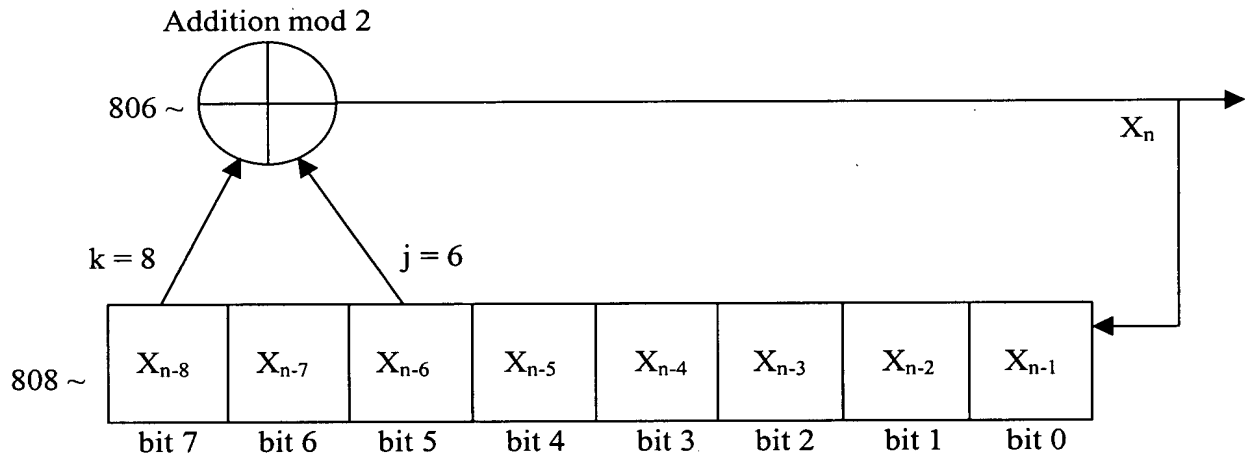
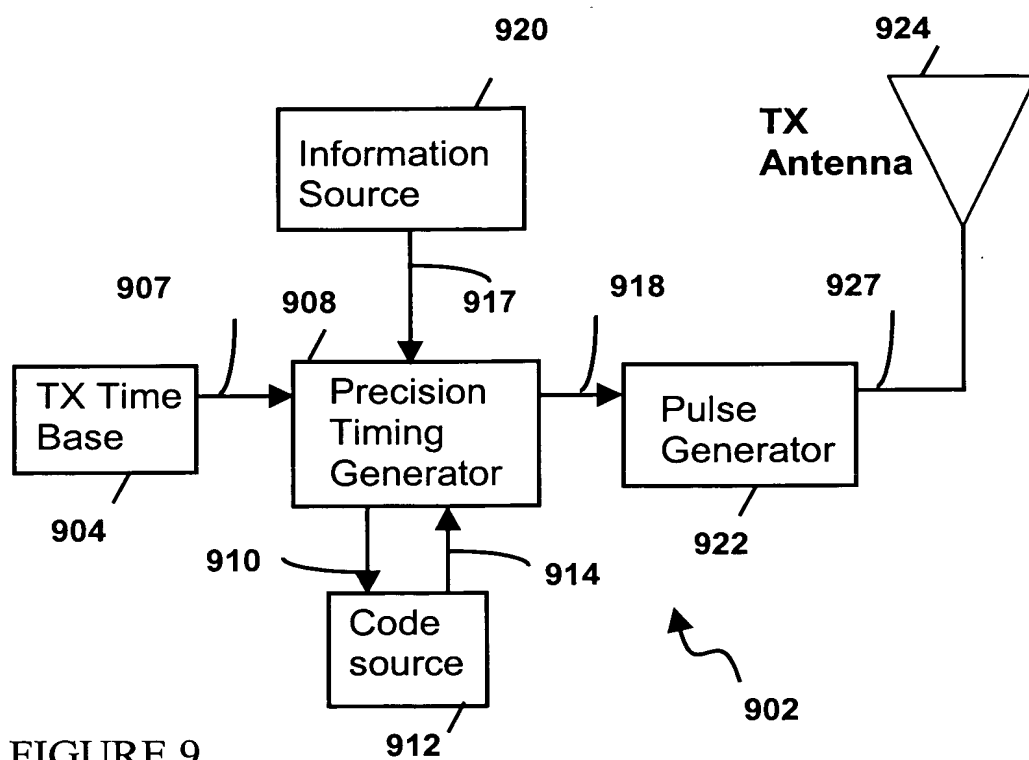


FIGURE 8b. Lagged-Fibonacci Shift Register Generator



005180" 26T8E960



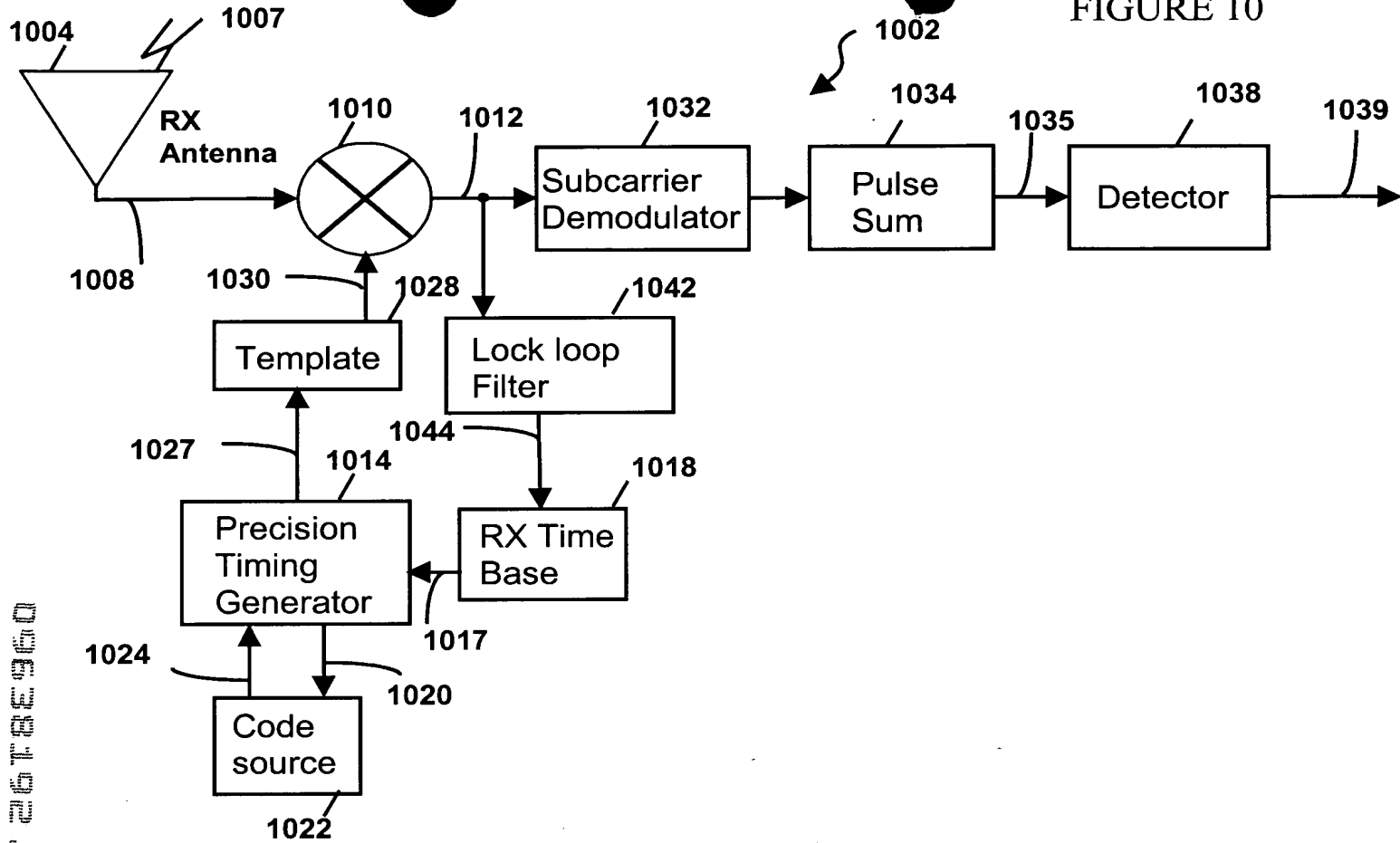


FIGURE 10

005780" 26T8E960